



Foreign electronic communications textbook series: Art of analog circuit layout (2nd edition) (English)(Chinese Edition)

By MEI) Alan Hastings

paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback. Pub Date: 2013 Pages: 664 Language: English Publisher: Publishing House of Electronics Industry foreign electronic communications textbook series: Art of analog circuit layout (2nd Edition) (English version) practical and authoritative view comprehensive discussion of the various issues involved in the analog IC layout design and the latest research. The book describes the content of the semiconductor device physics and technology. failure mechanisms; based on three basic process used in analog integrated circuit design: standard bipolar process. the polysilicon gate CMOS and analog BiCMOS process. focusing on the design of passive components matching problems. the design and application of diode design. bipolar transistors and field-effect transistor. and the contents of some of the areas of expertise. including device consolidation. protection ring. pad production. single connection. ESD structures. etc.; Finally The layout of the chip layout wiring knowledge. Contents: Chapter 1 Device Physics 1.1 Semiconductor 1.2 PN junction 1.3 bipolar transistor 1.4 MOS transistor 1.5 JFET transistor 1.6 Summary 1.7 Exercises Chapter 2 semiconductor manufacturing 2.1 silicon manufacturing 2.2 lithography the 2.3 oxide growth and removal of 2.4 diffusion and...

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